

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	16	memory.ti. and "cycle ready" same signal same next	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:03
S2	2	memory.ti. and "cycle ready" same output same signal same next	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:04
S3	4	memory.ti. and "cycle ready".ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:04
S4	63	memory.ti. and "cycle ready"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:05
S5	4	memory.ti. and "cycle ready" and ("self-clock" "self-clocking")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:05
S6	29	memory.ti. and ("self-clock" "self-clocking")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:07
S7	36	memory.ti. and (self adj clock\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:09
S8	661	memory.ti. and (clock\$3 same cycle same ready)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 12:44
S9	4	"cycle ready".ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:16
S10	105	self-clocking.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:16
S11	4	self-clocking.ti. and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 15:17
S12	689	memory.ti. and cycle same ready same operation	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 16:10
S13	80	memory.ti. and ((cycle near2 ready) same operation)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/20 16:12
S14	69	memory.ti. and ((cycle near2 ready) same clock\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 12:07
S15	158	self-clock\$3 same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 12:11

S16	16	self-clock\$3 same memory and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 12:08
S17	7	self-clock\$3 same cycle same ready	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 12:12
S18	56	memory.ti. and (clock\$3 same cycle same ready same strobe same control)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 15:36
S19	1	memory.ti. and (cycle adj start same strobe same control same ready)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 15:37
S20	25	memory.ti. and (cycle adj start same strobe)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/21 15:53
S23	4	self-clocking.ab. and "cycle ready strobe".ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 15:14
S24	1	ready same clock\$3 same strobe same (multiplex\$3 adj input)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 15:17
S25	9	ready same clock\$3 same (multiplex\$3 adj input)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 15:18
S26	151	ready same clock\$3 same strobe same multiplex\$3 same input	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 16:35
S27	18	ready same clock\$3 same strobe same multiplex\$3 same input and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 15:21
S28	29	ready same clock\$3 same strobe same multiplex\$3 and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 15:25
S29	155	ready same clock\$3 same multiplex\$3 and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 16:15
S30	38	ready same clock\$3 same controller same multiplex\$3 and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/24 16:15
S31	63	ready same clock\$3 same strobe same precharge	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/26 10:33
S32	5	ready same precharge same voltage same temperature	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/26 10:48
S34	188	precharge same ready same (voltage temperature)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/26 10:49